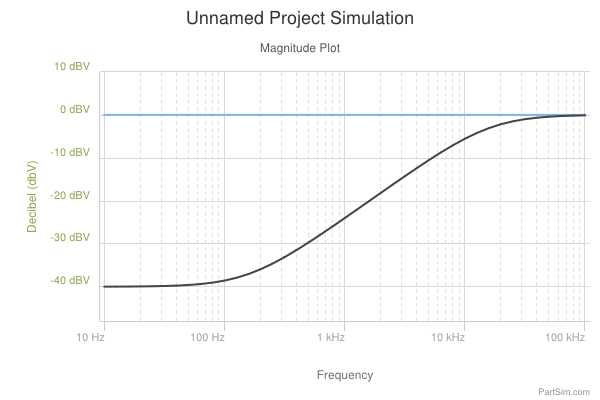
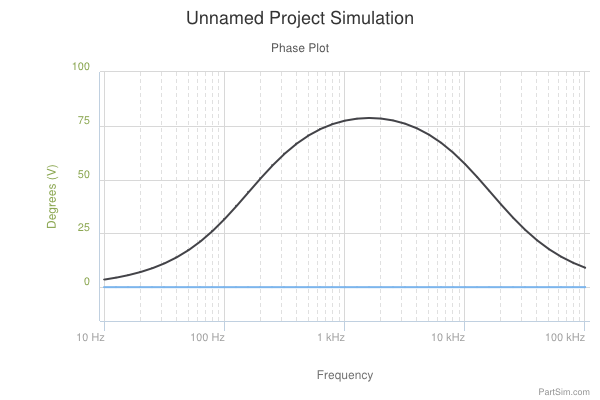
Circuit 4.1



Circuit 4.2

